PERKINS COIE LI.P (650) 838-4300
Title: Low Wander Timing Generation and Recovery
NEW PATENT APPLICATION
Atty. Dkt. No.: 59028-8002.US01
Express Mail No.: EV 336043037 US
Sheet 1 of 8

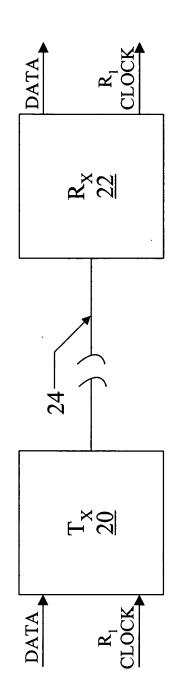


FIG. 1 (PRIOR ART)

Network  $R_1$  Clock 26

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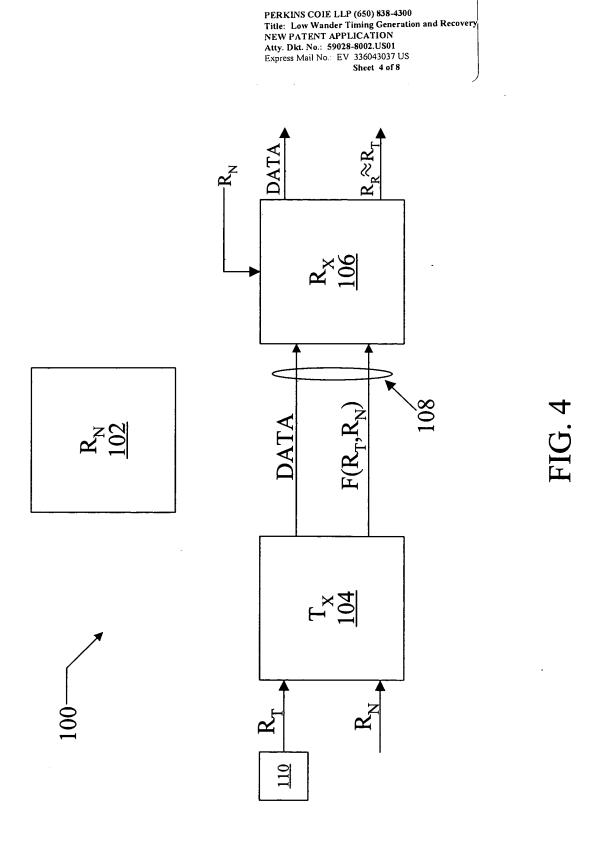
SYCNCHRONIZED DATA SIGNAL **PHASE** BUFFER FIFO <u>99</u> 89 62 09 58

FIG. 2 (PRIOR ART)

PERKINS COIE LLP (650) 838-4300
Title: Low Wander Timing Generation and Recovery NEW PATENT APPLICATION Atty. Dkt. No.: 59028-8002.US01 Express Mail No.: EV 336043037 US Sheet 3 of 8 80 -**START CALCULATE** -82  $F(R_T,R_N)$ **TRANSMIT** DATA & -84  $F(R_T,R_N)$ **RECEIVE DATA** &  $F(R_T,R_N)$ -86 &R<sub>N</sub> **ESTIMATE -88**  $R_T \approx R_R$ 

FIG. 3

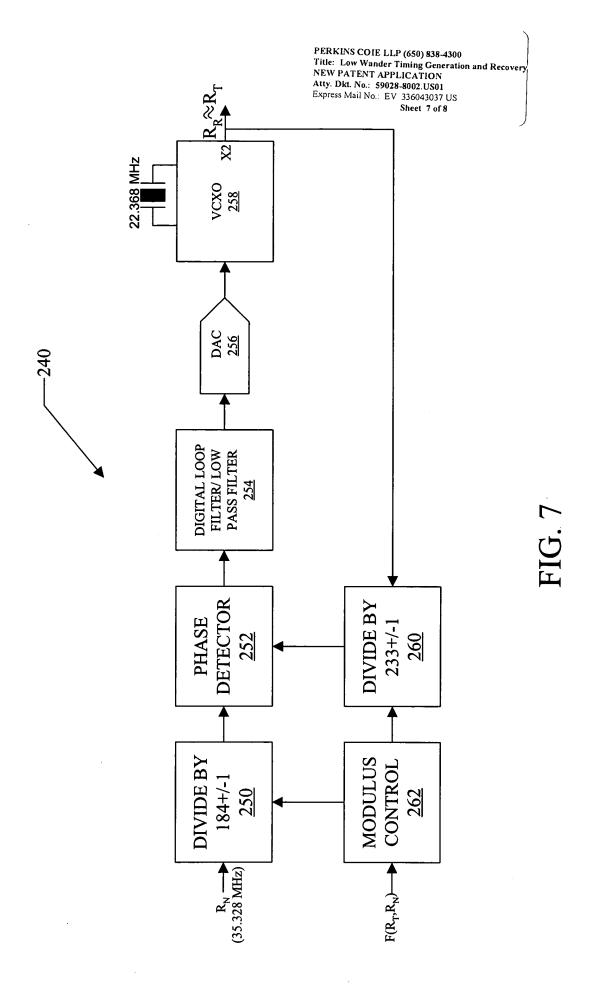
DONE



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Title: Low Wander Timing Generation and Recovery NEW PATENT APPLICATION Atty. Dkt. No.: 59028-8002.US01 Express Mail No.: EV 336043037 US Sheet 5 of 8 <del>1</del>56 PHASE DETECTOR 154 DIVIDER MODULUS CONTROL 158 +/- M 152 +/- N 150

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FIG. 6



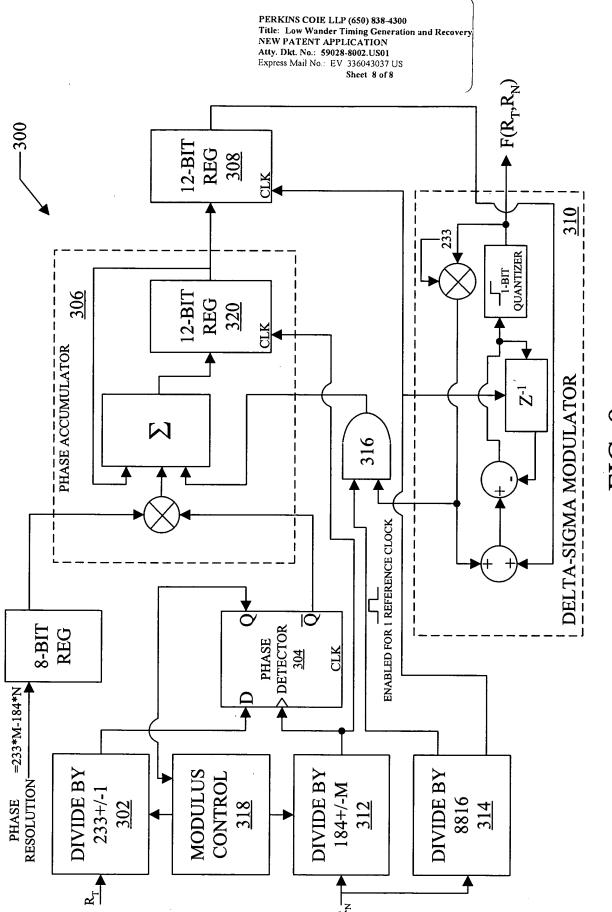


FIG. 8